

CONFERENCE RECORD

SEVENTH ASILOMAR CONFERENCE ON CIRCUITS, SYSTEMS, AND COMPUTERS

EDITED BY

Sydney R. Parker
Naval Postgraduate School
Monterey, California

PAPERS PRESENTED

TUESDAY THROUGH THURSDAY NOVEMBER 27-29, 1973
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AN EXPERIMENT WITH METHODOLOGIES FOR THE
SPECIFICATION OF PROGRAMMING SYSTEMS†

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Abstract

An experiment with programming is used as the basis for discussion of such topics as: formalisms for systems specification, hierarchical structure of systems and modularity and approaches for testing and debugging.

1. INTRODUCTION

The objective of this research was to analyze the possible impact of a number of emerging methodologies on the design and production of software. The current high cost of software indicates that considerable methodological improvements for software production are critical factors for the development of computer technology.

Although there are some programming experiments reported in the literature [1,2] none seems to approach the variety of aspects that are dealt with by this paper.

When developing a programming system from a natural language statement about an application problem a designer typically concerns himself with assuring a number of properties of the system. One useful classification for those properties is performance, correctness and structural suitability. In fact, the system is expected to work according to certain efficiency parameters, to reproduce the natural language statement of the problem with fidelity and to have certain structural attributes such as being modifiable, adaptable, etc. To formulate a set of synthesis rules and supply the convenient tools to carry on this task effectively is not easy, particularly when we deal with large systems. Among the possible questions to be answered for the achievement of this goal, the following were explored during our experiment: the need and characteristics of a formalism that can be used from the problem definition to the program implementation; hierarchical structure of programming systems versus modularity; programming languages characteristics and feature utilization and approaches for program testing and debugging. The emphasis of the following discussions will be

placed on systems which are required to be correct (in the sense mentioned above) and possess the structural characteristics of being changeable and adaptable.

2. THE PROBLEMS AND THE APPROACH TO THEIR SOLUTION

Our experimental problem is a simple MIS in which its programs solve some classical bookkeeping functions which exist in any academic administration. The example was considered convenient for the experiment due to its simplicity and representativity of data processing problems. The system can be schematically described through the steps in figure 1.

The breakdown of the programming system in the steps above illustrates the fact that five (one systems sort program) absolute modules could be used in the design. An absolute module is a program which communicates with other programs via a standard interface provided for by physical media. In absolute modules the synthesis rules for software modules can be satisfied [3,4] through the following requirements:

- (1) Syntactic non-interference (modules can be put together without changes).
- (2) Semantic context independence (modules can be tested independently).
- (3) Data generality (module writers can define arbitrary data structures and communicate through them).

This, of course, reduces the original design and implementation problem to a set of smaller sub-problems (one for each absolute module) each to be model-

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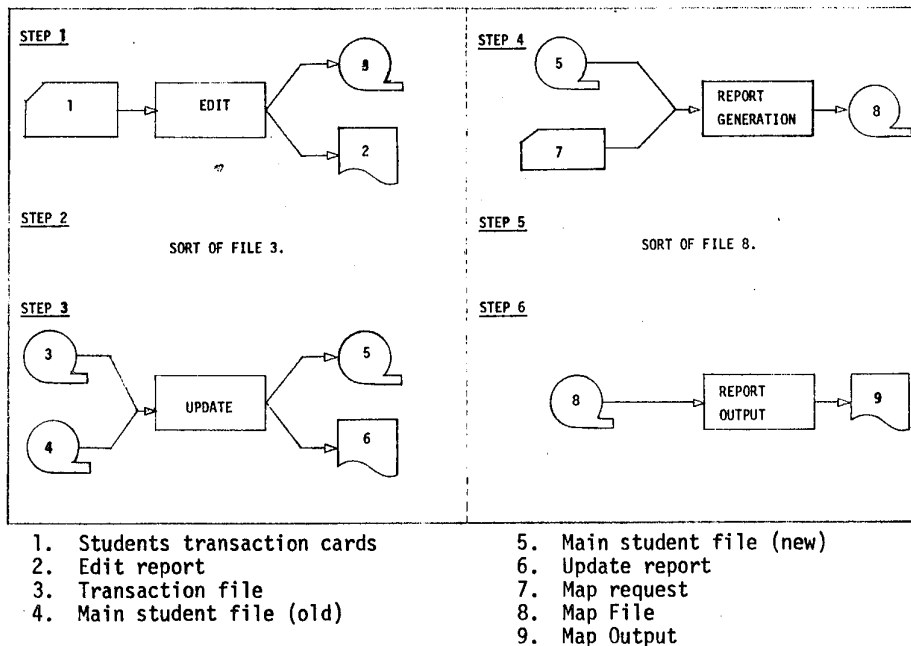


Figure 1. Outline of the example used for the experiment

ed by a programming sub-system. For each sub-system an attempt was made to identify software modules that could be implemented by different programmers.

The specification of each sub-system was expressed in terms of a specification language. For this purpose we adopted the notation proposed by Hoare in [5]. The characteristics we looked for in this notation were:

- a highly declarative language in which most of the problems could be absorbed by the data structure declarations.
- a language where the data structures utilized are of a higher level than those currently found in most programming languages (set theoretical data structures).

As it stands, the notation adopted constitutes a good approximation of these goals. In the specification language utilized, the designer specifies its own types and admissible domains and ranges of variables through enumeration, ordered enumeration and sub-ranges definitions. The data structures comprise structures such as Cartesian Products (multi-valued elements), Discriminated Union of Sets, Generalized Arrays, Powersets and Sequences. The examples below illustrate the referred entities.

The control structures used in conjunction with the mentioned data structures include such statements as: if then else, do while, case and for. The synthesis steps adopted for the construction of each sub-system were the following:

1. Characterization and definition, through declarations in the specification languages, of all the input and output variables and

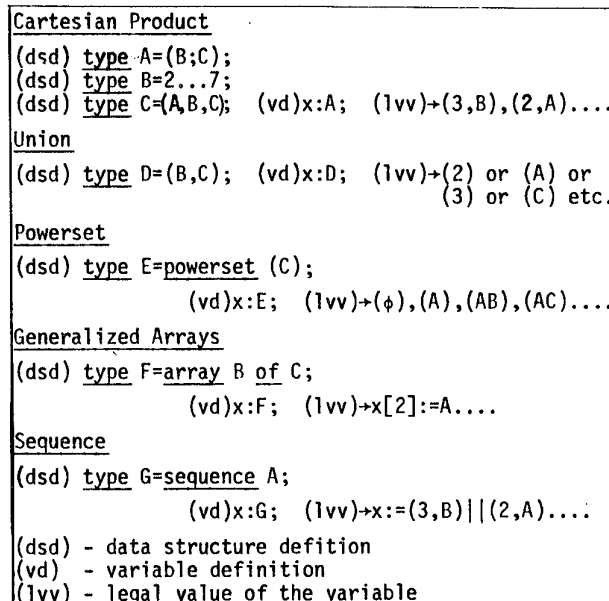


Figure 2. Data Structures in the Specification Language

associated domains and ranges.

2. Selection of the control and data flows of each algorithm taking into consideration the "well-formation" of its structure and in particular the issue of modularity. For this purpose alternative graphs of computation were built for each sub-system until a "good structure" was found. In figure 3 one of

such graphs is shown for the sub-system that models the step 3 of the problem. The algorithm shown is the well known balance line for the update of an old file based on information from a transaction file.

3. After achieving a "good structure" the sub-system is completely described in terms of the specification language.
4. Each module within a sub-system was coded and tested independently of the others. The programming language used for implementation was PL/I. Each module definition was comprised of the specification text and a set of test cases expressed in terms of the variables defined in the specification.

Figure 4 displays the specification and implementation texts in its final form. The specification,

which has no associated language processor, appears as comments to the PL/I program. The example is shown just to give the flavor of the notation and illustrate the aspect of the final software produced. We are choosing to show only some of the programs corresponding to step 3 which, in turn, is the smallest sub-system in the system.

The design and implementation strategies adopted are summarized and discussed in the next section. Through their application we were able to generate a program which was demonstrated to be easily changeable and which over extensive use after testing revealed no errors. The effort placed in the design phase did reduce the testing and debugging efforts. Although we do not claim that this methodology is optimal in any sense, its application did highlight a number of interesting topics that we are presently pursuing in our research.

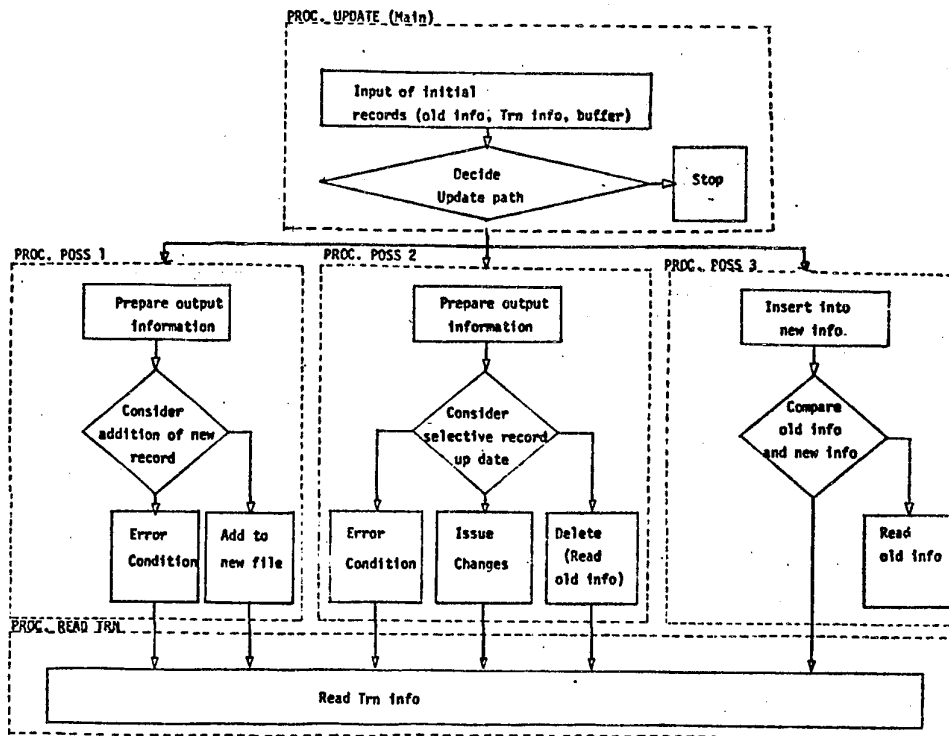


Figure 3. Graph Model associated to step 3 of the problem.

2. COMMENTS ON PROGRAMMING METHODOLOGIES

The design and construction of the software system described was carried on with a critical attitude towards the methodologies applied. We believe that the attitude itself of attempting to build a "good" programming system is responsible for a very high percentage of the quality we achieved for the system.

Despite the fact that we decided to break this section into two major topics, the reader should be aware that there exists a complete interaction between these and that the understanding of this interaction is a fundamental issue.

2.1 Hierarchical Structures and Modularity

Enough has been written about the advantages of imposing a hierarchical structure on a programming system. We contend that its most relevant advantage is the possibility of applying informal proofs of correctness to the system both at the design and implementation levels. This advantage is followed by the possibility of investigating duplication of efforts at the same and different levels of the systems tree. The order in which the design tree is produced is altogether a different matter. We do not think that the top down order of design is a natural or, much less, an inseparable companion of hierarchical structures. At this point we quote

```

PROCEDURE UPDATE(CLDINFO,TRNINFO)
*
*TYPE* MAIN STUDENT FILE = 'SEQUENCE' STUDENT_FILE;
*TYPE* STUDENT_TRANSACTIONS_FILE='SEQUENCE';
*TYPE* STUDENT_FILE=(STUD_RANGE;NAME;ADDRESS;DATE;
* SCOUNUM;CITY;STATE;CCOUNTRY;
* NEWDATA);
*TYPE* STUDENT_TRANSACTION=(STUD_RANGE;CCDRANGE;QUESTION;
* NAMES;ADDRESS;DATE;SCOUNUM;
* NEWDATA);
*TYPE* STUD_RANGE=CCCCCOL...55555599;
*TYPE* CCDCRANGE=1...4;
*TYPE* CCDCRANGE=1...30;
*TYPE* NAME=(FIRSTNAME;MIDDLENAME;LASTNAME);
*TYPE* ADDRESS=(STREET;CITY;ZIPCODE;PHONE);
*TYPE* DATE=(CERTAIN;MONTH;CERTAIN;YEARS);
*TYPE* SCOUNUM=CCCCCOL...99999999;
*TYPE* CCDCRANGE=CCCCCOL...99999999;
*TYPE* COUNTRY=(STATE;CCOUNTRY='SEQUENCE' LETTER;
* NEWDATA);
*TYPE* FIRSTNAME;MIDDLENAME;LASTNAME='SEQUENCE' LETTER;
*TYPE* STREET=(DIGIT;DIGIT);
*TYPE* CITY='SEQUENCE' LETTER;
*TYPE* ZIPCODE;PHONE='SEQUENCE' DIGIT;
*TYPE* LETTER=(A,B,C,...Z);
*TYPE* LINE=(IC;I;Z;R;R;9);
*TYPE* DIGIT='SEQUENCE' (DIGIT;LETTER);
*
*CLDINFO;NEWINFO; MAIN STUDENT FILE;
*TRNINFO;STUDENT_TRANSACTIONS_FILE;
*CLD_STUD;STUDENT_FILE;
*TRN_STUD;STUDENT_TRANSACTION;
*STUD_CODE;STUD_RANGE;
*
UPDATE: PROCEDURE OPTIONS(MAIN);
DCL 1 CLD_STUD CHAR(240);
DCL 1 CLD_STUD_DEFINED CLD_STUD(1);
DCL 1 STUD_CODE PIC(18)9;
DCL 1 FIRST PIC(12)X;
DCL 1 BUFFER1 CHAR(240);
DCL 1 BUFFER1_DEFINED BUFFER1(1);
DCL 1 STUD_CODE PIC(18)9;
DCL 1 TRN_STUD CHAR(180);
DCL 1 TRN_STUD_DEFINED TRN_STUD(1);
DCL 1 FILLER PIC(18)9;
DCL 1 LINE(1) INIT(' ');
DCL 1 LINE(2) CHAR(1) INIT(' ');
DCL PAGE=1;
DCL SWITCH;
DCL CLDINFO FILE RECORD INPUT;
DCL TRNINFO FILE RECORD INPUT;
DCL BUFFER=CLD_STUD;
DCL TRN_STUD=TRNINFO;
DCL TRN_STUD_CODE = 99999999;
DCL TRN_STUD_CODE = 99999999;
*
*IF TRN_STUD_CODE < BUFFER_STUD_CODE THEN
* PCSS3(CLD_STUD,BUFFER,TRN_STUD);
*ELSE IF TRN_STUD_CODE = BUFFER_STUD_CODE
* THEN
* PCSS3(CLD_STUD,BUFFER,TRN_STUD);
*ELSE PCSS3(CLD_STUD,BUFFER,TRN_STUD);
*
*END;
*
SWITCH = 1;
CALL PRINT(LINE1,LINE2,SWITCH);
OPEN FILE(TRNINFO);
FILE(NEWINFO);
READ FILE(CLDINFO) INTO (CLD_STUD);
READ FILE(TRNINFO) INTO (TRN_STUD);
BUFFER = CLD_STUD;
DCL WHILE (TRN_STUD_CODE = 99999999)
DO WHILE (TRN_STUD_CODE < BUFFER_STUD_CODE)
IF TRN_STUD_CODE < BUFFER_STUD_CODE THEN
CALL PCSS3(CLD_STUD,BUFFER,TRN_STUD);
ELSE IF TRN_STUD_CODE = BUFFER_STUD_CODE THEN
CALL PCSS3(CLD_STUD,BUFFER,TRN_STUD);
ELSE IF TRN_STUD_CODE > BUFFER_STUD_CODE THEN
CALL PCSS3(CLD_STUD,BUFFER,TRN_STUD);
*
*END;
CLOSE FILE(CLDINFO), FILE(TRNINFO), FILE(NEWINFO);

```

```

PROCEDURE POSS1(CLD_STUD,BUFFER,TRN_STUD)
*
*NAME;NAME;
*CARD_CODE=CCDRANGE;
*STUD_CODE=STUD_RANGE;
*WITH TRN_STUD 'CC' 'CASE' CARD_CODE 'CF'
* (01;'BEGIN' BUFFER_STUD_CODE=TRN_STUD_CODE;
* BUFFER_NAME=TRN_STUD_NAME;
* END;
* END;
* END;
* END;
* END;
* END;
*
POSS1: PROCEDURE (CLD_STUD,BUFFER,TRN_STUD);
DCL 1 CLD_STUD CHAR(240);
DCL 1 CLD_STUD_DEFINED CLD_STUD(1);
DCL 1 STUD_CODE PIC(18)9;
DCL 1 FILLER PIC(18)9;
DCL 1 DATE1 PIC(9)999;
DCL 1 LAST PIC(18)X;
DCL 1 MIDDLE PIC(14)X;
DCL 1 FIRST PIC(18)X;
DCL 1 REST CHAR(240);
DCL 1 BUFFER CHAR(240);
DCL 1 TRN_STUD_DEFINED TRN_STUD(1);
DCL 1 TRN_STUD CHAR(180);
DCL 1 FILLER PIC(18)9;
DCL 1 DATE1 PIC(9)999;
DCL 1 LAST PIC(18)X;
DCL 1 MIDDLE PIC(14)X;
DCL 1 FIRST PIC(18)X;
DCL 1 BUFFER3_DEFINED BUFFER;
DCL 1 TRN_STUD_DEFINED TRN_STUD(1);
DCL 1 TRN_STUD CHAR(180);
DCL 1 STUD_CODE PIC(18)9;
DCL 1 CARD_CODE PIC(2)9;
DCL 1 QUESTION;
DCL 1 NAME;
DCL 1 FIRST PIC(14)X;
DCL 1 MIDDLE PIC(14)X;
DCL 1 LAST PIC(18)X;
DCL 1 FILLER PIC(12)X;
DCL 1 LINE1 CHAR(1) INIT(' ');
DCL 1 LINE1_G_DEFINED LINE1;
DCL 1 P_STUD_CODE PIC(18)999;
DCL 1 P_CARD_CODE PIC(16)899;
DCL 1 P_QUESTION PIC(18)X;
DCL 1 P_X PIC(18)X;
DCL 1 P_FIRST PIC(18)X;
DCL 1 P_MIDDLE PIC(14)X;
DCL 1 P_X3 PIC(18)X;
DCL 1 P_ERROR1 PIC(2)9;
DCL 1 LINE2 CHAR(1) INIT(' ');
DCL 1 LINE2_G_DEFINED LINE2;
DCL 1 X4 PIC(18)X;
DCL 1 X5 PIC(18)X;
DCL 1 X6 PIC(18)X;
DCL 1 P_ERROR2 PIC(2)9;
DCL SWITCH;
*
LINE1, LINE2 = TRN_STUD1,STUD1;
P_STUD_CODE = TRN_STUD1, BY NAME;
P_CARD_CODE = TRN_STUD1, BY NAME;
P_QUESTION = TRN_STUD1, BY NAME;
P_FIRST = TRN_STUD1, BY NAME;
P_MIDDLE = TRN_STUD1, BY NAME;
P_LAST1 = TRN_STUD1, BY NAME;
IF CARD_CODE = 1 THEN
DO;
BUFFER2 = TRN_STUD1, BY NAME;
BUFFER1 = TRN_STUD1, BY NAME;
*
*END;
*ELSE P_ERROR1 = 'STUDENT NOT IN FILE';
*
*END;
CALL PRINT(LINE1,LINE2,SWITCH);
CALL READTRN(TRN_STUD);
*
*END POSS1;

```

Figure 4. Specification and Implementation texts for Part of Step 3

Goos: "design without iteration looks like throwing a ball into a hole; whether we succeed depends on the size of the hole and of the ball as well as on our knowledge about the position of the hole and our experience in throwing." An interesting discussion about order of design decisions can be found in [6]. We can illustrate this point through a situation that we observed during our experiment. One argument in favor of "pure" top-down design is that the if then else - do while - sequence Type of control structure completely takes care of the dimension of control in an informal definition of well structured sequential programs. It means that from the point of view of testability an ideal program would be generated by proceeding top-down using only those structures. Suppose now that in a given design process an instance of the following basic situation is found (figure 5).

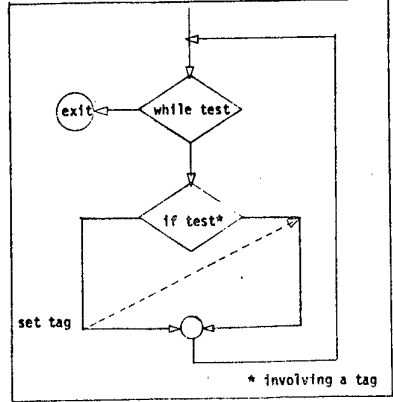


Figure 5. Dominant Tagging Effect

We called this situation a dominant tagging effect. In this case we are simulating a goto. All the undesirable aspect of gotos can be present when dominant tags are used. The designer has to decide whether to review the developing structure based on the size of the substructure that involves the tag and the eventual burden that it is adding to the program's testability.

When the complete hierarchical structure of a system is ready and informally proven correct, another dimension of well-formed programs has to be considered: the dimension of modularity. Not necessarily the partitions induced by the hierarchical structures (sets of nodes with the same degree) constitute themselves a good modularization. The following objections show in practice when attempting to use partitions directly as modules. When coding progresses bottom-up (just one level of language: implementation level), with the testing of the individual partitions being done in the same order, some very hard problems in debugging can appear. They are of the following nature: which level should be held responsible for a detected error [7]? In a top-down testing procedure, semantic context independence has to be enforced by the creation of program stubs [8] which besides being themselves additional sources of error may create some distortions to the system. The distortions appear when we try during the design phase to anticipate some decisions about input. This is done with the objective of producing better testing conditions at the early partitions of the hierarchy.

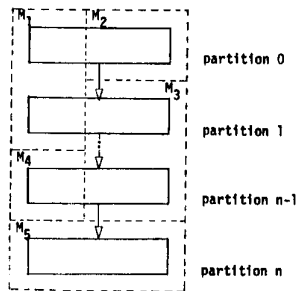


Figure 6. Module Configuration

We found that the process of defining modules can be effectively based on the systems hierarchical structure. The segmentation process that yields the modules may take place horizontally, vertically or both. The heuristics of the configuration is aimed at providing the software system with the characteristics of adaptability and changeability. This is achieved by defining modules which possess syntactic non-interference, semantic context independence, and data generality. The data structure interface between modules is the most difficult problem to be solved. We might find that if the modules do not form a class of equivalence under independent implementation the designer may be forced to provide more than one module to be implemented by the same programmer (programmers group). See [3] for a proposed solution. Two other activities take place during module configuration; identification of parallel processes and structure reconfiguration. By structure reconfig-

uration we mean the process of eventually transforming the tree of the system into a directed acyclic graph. This new structure proves to have the same advantages for testability as the preceding one and might reduce considerably some duplications of work. (figure 3 is a directed acyclic graph).

2.2. Language for Design Specification

In the discussions above we left undefined the notation used to express the design. We think that this is a very critical point for the quality of the software product being designed. The proof of correctness type of approach or any other type of testing will try to match a given program with the documentation about the program [9]. If the documentation is incomplete or ambiguous, any effort towards program validation is useless. In terms of proof of correctness it would be said that assertions cannot be generated. Of course, this concern grows considerably as the complexity of the system increases.

It is not difficult to estimate the amount of extra design information that such a design as that illustrated in figure 7 would require (example from [8]).

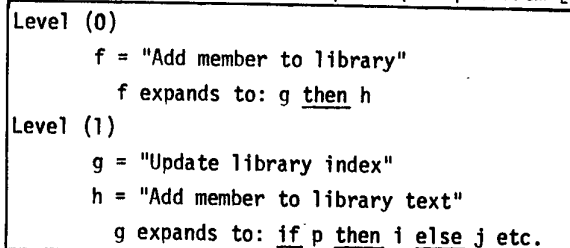


Figure 7. Need for Specification Language

We considered two notations for the present experiment. One proposed by Parnas [10] and another proposed by Hoare [5]. Parnas notation seems to apply better when the modules are already known and implementation decisions can begin. Hoare's notation seems more adequate to express the design decisions from an early stage up to, excluding, the implementation phase. We have not considered their combination although this is an interesting possibility.

The notation adopted in our experiment provided a very efficient communication mechanism between the authors. From the implementation point of view, with the use of the documentation provided by the specification text, the task of programming could approximately be described as the embedding of statements between "assertions" about the program. Together with each module specification the designer is able to provide a set of input data (in terms of the specification language data structures) that exhaustively tests the design. Those test cases can be adapted and used at the implementation level.

The acceptance of the implementation took place when a systematic inspection of the specification by the programmer was accomplished. Although it was applied manually we have hopes that this procedure will be even more effective when applied semi-automatically. The implementation of different modules could conceivably be carried out in different programming languages.

Experience with the language has indicated a number of possible improvements. Some have to do with enforcing some data protection mechanisms that the programmer will follow in the implementation. Others have to do with both language features and coding techniques at the specification level. We would like the programmer to receive from the specification phase, all the assertions that would be required for a proof of correctness in the sense of [9] to be carried out. Even if he is not proving that the implementation is correct, it would considerably strengthen the method for acceptance of implementation mentioned before.

3. CONCLUSIONS

The questions of completeness and consistency of design were only dealt with informally in the exercise. Testability, modularity and informal proof of control correctness were the criteria used for design acceptance. We felt that a more formal interconnection can be established between the textual form of the design in the specification language and their respective graph models of computation. We lacked a tool that would take the form of a "data dependency table" and that could also be formally related to the two previously mentioned mechanisms. If such formal interconnection is achieved some very interesting semi-automatic procedures can be developed to deal with the issues of completeness and consistency of design. That, plus a partial mechanization of the implementation validation would give us a hope that more generally applicable methodologies could be produced.

We were strongly tempted to use analogues of the absolute modules referred to in section 1 to solve the problem of providing modules with data generality. For this purpose we sketched with considerable loss in efficiency some software simulators of standard interfaces that would have solved the problem. This idea together with the previous ones could serve as the basis for the design of a convenient programming environment for program development.

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